IN THE SPECIFICATION

Please amend the abstract as follows:

herein. A transmitting circuit in a data communication apparatus converts frame data into serial data and transmit transmits the same, and following the serial data, the transmitting circuit transmits frame synchronization data varying several times in the interval from an edge of a clock signal to an edge of the next clock signal; a receiving circuit receives the frame data and detects twice two or more variations in the same interval to find out the end of the frame data, by receiving the serial data from a signal lines line, serial data is transmitted while carrying out frame synchronization.--

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IN THE SPECIFICATION

At page 42, please replace the paragraph beginning on line 9 and ending at line 14 with the following:

Further, in the rest Furthermore, concerning the rest of the signal lines, by detecting two or more changes of the transmitted data SD in the same interval, it is possible to transmit additional data to the as frame synchronization data. The additional data may include parity data for checking errors present in data and or check sum data, and so on.

Please replace the paragraph beginning at page 49, line 23 and ending at page 50, line 4 with the following:

As described above, according to the present invention, there ean be is provided a data communication apparatus of a new configuration that is able to transmit data while carrying out frame synchronization., and a Also, a transmitting circuit and a method thereof and a receiving circuit and a method thereof that are able to be used in the data communication apparatus are provided. Still further, a transmitting method and a receiving method are provided.